EM3568-AV CAM User Manual

V1.0



Boardcon Embedded Design

www.armdesigner.com



Preface

Overview

The content of this document is only described for the development board EM3568-AV CAM, aiming to help users quickly understand, apply and develop the EM3568-AV CAM development board.

System Support

Development Board	Android12	Debian11	Buildroot
EM3568-AV CAM_V1	N	V	Ν
CM3568_V2.0	IN	T	IN

Revision History

Version	Date	Author	Revision History
V1.0	2023-06-19	Liu Yuan	Initial version

Version update instructions

Due to product version upgrades or other reasons, the content of this document will be updated from time to time. At the same time, it will be synchronized to the Boardcon website.

If you have any questions, concerns or comments about what is stated in the document, please feel free to contact us.

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Introduction

1.Overview

EM3568-AV CAM is a development board designed based on Boardcon CM3568 system on module. The CM3568 SOM is powered by Rockchip RK3568 which is a quad-core ARM Cortex-A55 processor with low power consumption and high performance, support almost full-format H.264 decoder by 4K@60fps, H.265 decoder by 4K@60fps, also H.264/H.265 encoder by 1080p@60fps, high-quality JPEG encoder/decoder and designed for personal mobile Internet devices and AloT devices.



2.Product Parameters

Basic Parameters		
SOC	RK3568	
CPU	Quad-core 64-bit ARM Cortex-A55@ up to 2.0GHz	
	• ARM G52 2EE	
GPU	• Support OpenGL ES 1.1/2.0/3.2, OpenCL 2.0 and Vulkan 1.1	
	Embedded high-performance 2D acceleration hardware	
NPU	Support 1T computing power	



		Support INT8/INT16/FP16/BFP16 MAC hybrid operation	
	Decoder	Support 4096x2304@60fps H.265/H.264/VP9 video decoding	
Video		Support 1920x1088@60fps VP8/VC1/MPEG-4,2,1 video decoding	
		Support 720x576@60fps H.263 video decoding	
	E	Support 1920x1080@60fps H.265/H.264 video encoding	
	Elicodei	Support YUV/RGB video source with rotation and mirror	
RAM		2GB LPDDR4X	
ROM		8GB EMMC	
Support syste	m	Debian11	
		Hardware Parameters	
Extended Sta	rada	Support SATA	
	lage	Support Mirco SD Card	
Display		Support LVDS 1280X800@60fps display	
Display		• Support CVBS display	
		Support Headphone output	
Audio		• Support Speaker output	
		• Support 3x USB2.0	
036		Support USB3.0	
Ethernet		Support 2x Gigabit Ethernet	
Minalaga Nati		Support WIFI/BT module	
wireless new	VOFK	Support 4G module	
Camera		Support 4x AHD IN	
		Support CAN	
Derinheral ac	munication	Support RS485	
Penpheral col	IIIIuiication	Support 3x UART	
		• Support 2x SPI	
Other parameters		Support Debug, USB OTG, RTC, GPIO	
Electrical Parameters			



Power supply input voltage	12V/3A	
RTC input voltage	3V	
Operating temperature	0~70°	
Storage temperature	-40~85°	
Structural Parameters		
Core board dimensions	60mm x 45mm	
Motherboard dimensions	135mm x 113mm	

3. Hardware Interface



Interface parameters		
Power in	12V DC power input interface	
USB2.0 Host	USB expansion interface	



Headset	Earphone output
2xUSB2.0 Host	Dual-layer USB2.0 HOST interface
RTC	RTC coin cell connector
2x Gigabit Ethernet	Gigabit Ethernet RJ45 interface, via Realtek RTL8211F-CG controller
USB3.0 Host	USB3.0 Host interface
CVBS OUT	CVBS display interface
USB OTG	OTG download interface
mPCle 4G	The EC20 4G module is used by default
Micro SD	MicroSD card slot
SIM Card	SIM card port
SATA0	SATA0 interface
SATA2	SATA2 interface
Recover	Recovery key
Reset	Reset key
Power	Power key
eDP	eDP screen display interface
LVDS	LVDS screen display interface
Debug	UART2, debug the serial port
Key connector	Including PWM3_IR/Reset/Recover/Power GPIO
4x AHD IN	AHD camera interface
SPI0	SPI0 interface
WIFI&BT	Realtek RTL8723DU WIFI/BT module
RS485	RS485 interface
SPI2	SPI2 interface
CAN	CAN interface
GPIO	GPIO/I2C5 extension interface
Speaker	Speaker interface

2x SATA Power	SATA power interface	
	• UART5, TTL level interface	
ZXUART	• UART4, TTL level interface	
12V input	12V DC power input gpio interface	
Power in	12V DC power input interface	

Install Drivers and Tool

To download firmware and debug in the terminal, the following drivers and software need to be installed(for Windows computers):

Number	Driver name	Driver	Use
1	RK Driver Assitant	DriverInstall.exe	OTG USB driver installation assistant
2	CP210x	PreInstaller.exe	Serial port debugging driver
3	Serial Terminal Tool	SecureCRT.exe	Debugging tool

1.Install RK Driver Assitant

Step 1, open DriverAssitant_v5.12/DriverInstall.exe.

Step 2, to avoid driver conflicts, click Uninstall Driver to uninstall the driver.



Step 3, click button Install Driver to install.

RK Driver Assitant	\times
Install Driver	

Step 4, after the installation is complete, connect the board and PC with Micro USB cable and press the **Recovery** key and hold then power the board, the following information is displayed in the Computer Device Manager, indicating that the USB driver was successfully installed.



Step 5, if the following device information appears on the computer device manager after the operation in step 4, you need to proceed to the next step.



Step 6, the WINDOW will pop up found New Hardware Wizard dialog box, choose to install from the specified location, and then select: \DriverAssitant_v5.12\ADBDriver.

Step 7, after the installation is completed, the following device information can be seen in the Computer Device Manager.



2.Install CP210X Driver

2.1 How to connect the serial port tool



Pin	Connection Description
3V3	No need to connect.
TXD	Transmit, connect to TX pin of the board.



RXD	Receive, connect to RX pin of the board.
GND	Ground, connect to GND pin of the board.
+5V	No need to connect.



Pin	Connection Description
1	RX, connect to RXD pin of the CP210X Module.
2	TX, connect to TXD pin of the CP210X Module.
3	Ground, connect to GND pin of the CP210X Module.

2.2 Install driver

Step 1, plug the CP2102 Module to the PC.

Step 2, unzip CP210x_Windows_Drivers_with_Serial_Enumeration.zip on Windows.

Step 3, select and install the corresponding PreInstaller.exe according to the computer properties.



Step 4, after the installation is completed, the device will be listed under **Device Manager -> ports** with unique serial port assigned.

Silicon Labs CP210x USB to UART Bridge (COM3)

3.Install Serial Terminal Tool

The serial terminal SecureCRT is used for debugging in Windows. It can be used directly after decompression.

Step 1, unzip Platform/SecureCRT.rar on PC.

Step 2, click SecureCRT/SecureCRT.exe open the SecureCRT.



🔲 no	t conr	nected -	SecureCR	Г							_	- C		×
File	Edit	View	Options	Transfer	Script	Tools	Window	Help						
- 2 - 2] []	47 X	Enter host	<alt+r></alt+r>		12 A	-6 50	9 8	28 1	0	-7			Ŧ
Ready					0, 0	0 0	Rows, 0 C	ols				C	AP NU	м

Step 3, confirm that the CP210x driver has been installed and the CP2102 module is connecting to the PC.

Step 4, click the Quick Connect button to go to the Quick Connect configuration screen.

not connected - Se	cureCRT				_		\times
File Edit View O	ptions Transfer S	cript Tool	Window Help				
🖏 🔀 🖓 🖏 🕅 Ent	er host <alt+r></alt+r>		1 5 5 6 6 %	10	-		÷
	Quick Connect			×			
	Protocol: SSH	2	~				
	Hostname:						
	Port: 22	Fire	wall: None	~			
	Username:						
	Authentication						
	Password		Properties				
	Keyboard Intera	ctive	-				
	[V]GSSAP1						
	Show quick connec	t on startup	Save session				
			Connect	Cancel			
			Connect	Cancel			
Ready		0, 0	0 Rows, 0 Cols			CAP N	UM

Step 5, configure as shown in the following figure.



盾 not connected - Se	cureCRT			- 🗆	\times
File Edit View O	ptions Transfer Script Too	ols Window Help			
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	ter host <alt+r> Quick Connect Protocol: Serial Port: COM3 ~ Baud rate: 1500000 ~ Data bits: 8 ~ Parity: None ~ Stop bits: 1 ~</alt+r>	Here Control	 × 		Ŧ
	Show quick connect on startup	Save session Sove session Connect Connect			
Ready	0, 0	0 Rows, 0 Cols		CA	NUM

Step 6, after clicking connect button, the terminal serial interface will be successfully accessed.

🖪 se	rial-co	m3 - Se	ecureCRT							_	- 🗆	×
File	Edit	View	Options	Transfer	Script	Tools	Window	Help				
23) []	i 🔊	Enter host	<alt+r></alt+r>		1 CL #1	- 6 5 (3 😚	81			Ŧ
؇ ser	ial-cor	m3 ×										4 ⊳
												^
												~
Ready			Serial: C	COM3, 1500	000 1	, 1 24	4 Rows, 80	Cols	/T100		CA	P NUM



Debian11

1.Compiler Environment

It is recommended to use Ubuntu 22.04 system or above. If you encounter an error during compilation, you can check the error message and install the corresponding software packages accordingly. Other Ubuntu versions may need to adjust the software package accordingly. In addition to the system requirements, there are other hardware and software requirements.

Hardware requirements	Software requirements
64-bit system, hard disk space should be greater than 40G. If you do	Ubuntu 22.04 system
multiple builds, you will need more hard drive space.	

2.Install Tools

The contents of this directory only provide the software package installation commands that are needed to build the compiled SDK environment. Please install other tools such as samba and ssh yourself.

PC OS: ubuntu 22.04 system

Network: online

Permission: root

- # sudo apt-get install git ssh make gcc libssl-dev liblz4-tool
- # sudo apt-get install expect g++ patchelf chrpath gawk texinfo chrpath diffstat
- # sudo apt-get install binfmt-support qemuuser-static live-build bison flex fakeroot
- # sudo apt-get install cmake gcc-multilib g++-multilib unzip device-tree-compiler
- # sudo apt-get install ncurses-dev libgucharmap-2-90-dev bzip2 expat gpgv2
- # sudo apt-get install cpp-aarch64-linux-gnu g++-aarch64-linux-gnu
- # sudo apt install python2
- # sudo apt install python-is-python3

3.Compile Source

Step 1, unzip the source

\$ tar xvf linux-5.10.tar.bz2

\$ cd linux-5.10

Step 2, configure the compiled board

\$./build.sh lunch

name@boardcon:~/opt/EM3568-TU/linux-5.10\$./build.sh lunch

Log saved at /home/name/opt/EM3568-TU/linux-5.10/output/log/2023-06-19_09-40-22



Pick a defconfig:

- 1. rockchip_defconfig
- 2. rockchip_rk3566_evb2_lp4x_v10_32bit_defconfig
- 3. rockchip_rk3566_evb2_lp4x_v10_defconfig
- 4. rockchip_rk3568_evb1_ddr4_v10_32bit_defconfig
- 5. rockchip_rk3568_evb1_ddr4_v10_defconfig
- 6. rockchip_rk3568_evb2_lp4x_v10_defconfig
- 7. rockchip_rk3568_uvc_evb1_ddr4_v10_defconfig

Which would you like? [1]: 6 // Select rockchip_rk3568_evb2_lp4x_v10_defconfig

Switching to defconfig: /home/name/opt/EM3568-TU/linux-

5.10/device/rockchip/.chip/rockchip_rk3568_evb2_lp4x_v10_defconfig

Step 3, compile uboot

\$./build.sh uboot

Step 4, compile the kernel

\$./build.sh kernel

Step 5, compile recovery

\$./build.sh recovery

Step 6, compile debian

\$./build.sh rootfs

After compilation, generate linaro-rootfs. img in the Debian directory.

Note: If it is the first-time compiling Debian, it required install dependency packages. (Permission: root)

- # cd debian
- # sudo apt-get install binfmt-support qemu-user-static live-build

sudo dpkg -i ubuntu-build-service/packages/*

sudo apt-get install -f

Step 7, generate and check firmwares

\$./build.sh firmware

Step 8, build update image

\$./build.sh updateimg

Images and update.img are generated in rockdev/ directory.

4.Images Operation

4.1 Unzip firmware

Step 1, open RKDevTool_Release\RKDevTool.exe.



Step 2, click Advanced Function -> Firmware, select update.img, then click Unpack to unzip.

KKDevTool v3.15	- 🗆 X
Download Image Upgrade Firmware Advanced Function Boot Download Firmware D:\Desktop\RK3568\update.img Unpack	Start to unpack union firmware Unpack union firmware ok Start to unpack android firmware Unpack android firmware ok,store in the output dir of tool
ReadFlashID ReadFlashInfo ReadChipInfo ReadCapability 1. FL 3. Zemmc 3. SD SD 3. SD TestDevice ResetDevice Go Maskrom Switch Storage 6. SPTINATD 7. RAM B. USB 7. RAM 8. USB 9. SATA ClearSn DetectSecure ExportComLog t Selected Stora 9. SATA	
ExportImage EraseLBA EraseAll Start Count:	
No Devices Found	~

Step 3, the unzip files will be generated in \RKDevTool\RKDevTool_Release\Output\Android and \RKDevTool\RKDevTool_Release\Output\Android\Image directory.

RKDevTool > RKDevTool_Release > Output > Android >
名称 ^
📙 Image
MiniLoaderAll.bin
📄 package-file
📄 parameter.txt
RKDevTool > RKDevTool_Release > Output > Android > Image
名称 ^
boot.img
🖉 misc.img
oem.img
incovery.img
💿 rootfs.img
📓 uboot.img
🥘 userdata.img

4.2 Pack image

Step 1, copy the firmware file to be packaged to windows RKDevTool/rockdev/Image.





\sim	4 5
1	ሳታ

	boot.img
e	MiniLoaderAll.bin
	misc.img
	oem.img
ľ	parameter.txt
	recovery.img
	rootfs.img
븕	uboot.img
	userdata.img

Step 2, enter RKDevTool/rockdev/, double-click to run rk356x-mkupdate.bat.

F	KDevTool > rockdev	
	名称	^
	Image	
	AFPTool.exe	
	📄 package-file	
	recover-script	
	💿 rk356x-mkupdate.bat	
	📄 rk356x-package-file	
	💿 rk1808-mkupdate.bat	
	📧 RKImageMaker.exe	
	update-script	
1.65		



Step 3, the update.img will be generated in rockdev/ directory.





5.Burn Images

5.1 Burn update firmware

Step 1, open RKDevTool_Release\RKDevTool.exe.

Step 3, connect PC and development board with Micro USB cable, keep pressing the **Recovery Key** and power the board until the windows PC shows **Found one LOADER Device** release the **Recovery Key**.





Step 4, click Upgrade Firmware -> Firmware, select update.img, then click Upgrade to flash.



KKDevTool v3.15	- 🗆 X
Download Image Upgrade Firmware Advanced Function Firmware Upgrade Switch 2 3 Fw Ver: 1.0.00 Loader Ver: 1.01 Chip Info: RK3568 Firmware: D:\Desktop\RK3568\update.img	Test Device Start Test Device Success Check Chip Success Get FlashInfo Success Prepare IDB Start Prepare IDB Start Download IDB Start Download IDB Success Download Firmware Start Download Firmware Success
Found One ADB Device	

5.2 Burn split firmware

Step 1, select the checkbox on the left.

Step 2, click the column on the right side for the path of the file want to flash.

Step 3, click **run** button to flash the image.

RKDe	evToo	ol v3.15				
nl.	ad Tr	10.00 11 1	. T ²	1 7		
	au 1	wage Upgrad	le firmware Ad	anced Functio	n	
	0	a.				2
# 1	ъ.	Storage	Address	Name	Tath	
,	Ť		0x00000000	parameter	\rockdev\parameter.txt	
3			0x00004000	uboot	\rockdev\uboot.img	
L	~		0x00006000	misc	\rockdev\misc.img	
	$\overline{\mathbf{v}}$		0x00008000	boot	\rockdev\boot.img	
;	~		0x00028000	recovery	\rockdev\recovery.img	
			0x00068000	backup		
:			0x01C78000	oem	\rockdev\oem.img	
)			0x00078000	rootfs	\rockdev\rootfs.img	
10	V		0x01D18000	userdata	\rockdev\userdata.img	
			•			
		_	3			
.oa	der:		Run	Switch	Dev Partition Clear	
			Write by Addre	55		
		_				
	P	. 1 0	LOADED	D	1-1 IDADER	~
	FOU	ina Une	LUADER	Device	1 1 . D. D. M.	

Step 4, wait for the completion of burning.



RKDe	vTo	ol v3.15				– 🗆 X
ownlo	ad Ir	mage Upgrad	e Firmware Ad	vanced Functio	n	Get FlashInfo Success Prepare IDB Start
#		Storage	Address	Name	Path	Prepare IDB Success Download IDB Start
1			0x00000000	loader	\rockdev \Image \MiniLoaderAll. bin	Wait For Loader Start
2			0x00000000	parameter whost	\rockdev\Image\parameter.txt	Wait For Loader Success
4			0x00006000	misc	\rockdev (Image \misc. img	Test Device Start
5	, ,		0x00008000	boot	\rockdev\Image\boot.img	Test Device Success
6	~		0x00028000	recovery	\rockdev\Image\recovery.img	DownLoad Gpt(100%)
7			0x00068000	backup		Start to download uboot
8	V		0x00C78000	oem	\rockdev\Image\oem. img	Start to download misc
9			0x00078000	rootfs	\rockdev\Image\rootfs.img	Download misc (100%)
10			0x00CB8000	userdata	\rockdev\Image\userdata.img	Start to download boot
						Download boot(100%) Start to download recovery Download recovery(100%) Start to download oem
Load	ier Ve	er:1.01	Run Write by Addre	Switch	Dev Partition Clear	Download cem(100%) Start to download rootfs Download rootfs(100%) Start to download userdata
	F	ound Or	ne ADB De	evice	1-1 :ADB	Download userdata (100%) Download image OK

6.Debian Test

6.1 Display

Supports LVDS and TV display. If you want to use the LVDS screen touch, you need to do the following to bind TP to the LVDS screen:

Step 1, installing xinput:

apt-get update

apt-get install xinput

Step 2, view display name:

xrandr

root@linaro-alip:/# xrandr Screen 0: minimum 320 x 200, current 2560 x 800, maximum 16384 x 16384 LVDS-1 connected primary 1280x800+0+0 (normal left inverted right x axis y axis) 0mm x 0mm 1280x800 59.19*+

Step 3, view the entered device ID:

xinput

root@linaro-alip:/# xinput		
[Virtual core pointer	id=2	[master pointer (3)]
ե Virtual core XTEST pointer	id=4	[slave pointer (2)]
4 Microsoft Microsoft Basic Optical Mouse	id=7	[slave pointer (2)]
4 goodix-ts	id=11	[slave pointer (2)]
Virtual core keyboard	id=3	[master keyboard (2)]
4 Virtual core XTEST keyboard	id=5	[slave keyboard (3)]
⊾ adc-keys	id=6	[slave keyboard (3)]
↓ rk805 pwrkey	id=8	[slave keyboard (3)]
↓ fdd70030.pwm	id=9	[slave keyboard (3)]
↓ hdmi_cec_key	id=10	[slave keyboard (3)]
root@linaro-alip:/#		

Step 4, bind TP:

xinput map-to-output [ID] [display name]

```
root@linaro-alip:/#
root@linaro-alip:/# xinput map-to-output 11 LVDS-1
root@linaro-alip:/#
```

6.2 SD/USB device

After inserting an SD card or USB device, a corresponding icon will be formed on the desktop.



Automatic mounting of SD/USB storage devices can be achieved through the following two methods.



Step 1, double click on the desktop icon and the SD/USB storage device will automatically mount:



Step 2, double click on the Home icon to enter the main directory:



Then click on the **Devices** under the devices directory to achieve automatic mounting:





The USB Host can be used to connect USB mouse, USB keyboard, U-Disk or other USB devices.

6.3 SATA

Connect the SATA0 and the sata power to the board. Execute follow command to mount SATA.

# ls /dev	// view sata device name
# mkdir /mnt/sata	// create a new directory file
# mount /dev/sda1 /mnt/sata/	// mount sata to the new directory
# ls /mnt/sata/	// view the contents of the sata

root@linaro-alip:~# mkdir /mnt/sata								
root@linaro-alip:~# mount /dev/sda1 /mnt/sata								
root@linaro-alip:~# ls /mnt/sata								
'\$RECYCLE.BIN'	Docur	nents	No	tific	ations		test	
2.wav	Down	load	Pi	cture	S		test.wav	
Alarms	Go-ho	ome.mp)3 Po	Podcasts			test~	
Android	L0ST.	.DIR	Re	cordi	ngs			
Audiobooks	Movie	es	Ri	ngton	les			
DCIM	Musia	C	bi	.g_buc	k_bunny_1080p_h264.mp	4		
root@linaro-ali	p:~#							
root@linaro-ali	p:~# di	f-h						
Filesystem	Size	Used	Avail	Use%	Mounted on			
/dev/root	14G	3.6G	9.6G	27%				
devtmpfs	3.8G	8.0K	3.8G	1%	/dev			
tmpfs	3.9G	Θ	3.9G	0%	/dev/shm			
tmpfs	1.6G	1.8M	1.6G	1%	/run			
tmpfs	5.0M	4.0K	5.0M	1%	/run/lock			
tmpfs	3.9G	16K	3.9G	1%	/tmp			
/dev/mmcblk0p7	121M	12M	101M	11%	/oem			
/dev/mmcblk0p8	15G	36K	14G	1%	/userdata			
tmpfs	793M	28K	793M	1%	/run/user/0			
tmpfs	793M	40K	793M	1%	/run/user/1000			
/dev/sda1	112G	851M	111G	1%	/mnt/sata			
root@linaro-ali	p:~#							

6.4 Video player

You can choose multiple commands to play videos: *Step 1*, this method is quite troublesome and mainly used for playing videos in H256 format: # cat /sys/kernel/debug/dri/0/state | grep "plane\[" // Select the display for playback





6.5 Ethernet

ifconfig





ping -I eth0 www.boardcon.com

ping -I eth1 www.boardcon.com

root@inaro-alip:/#
Prote innaro-alip:// ping -1 etho www.boardcon.com
PING www.boardcon.com (67.222.54.196) from 192.168.0.139 etho: 56(84) bytes of data.
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): 1cmp_seq=1 tt1=47 time=212 ms
64 bytes from 6/-222-54-196.unifiedlayer.com (6/.222.54.196): jcmp_seq=2 tt]=4/ time=212 ms
64 bytes from 67-222-54-196.unified[ayer.com (67.222.54.196): jcmp_seq=3 tt]=47 time=212 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=4 tt]=47 time=212 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=5 ttl=47 time=212 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=6 ttl=47 time=215 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=7 ttl=47 time=213 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=8 ttl=47 time=211 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=9 ttl=47 time=213 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=11 ttl=47 time=212 ms
64 bytes from 67-222-54-196.unified]aver.com (67.222.54.196): icmp_seg=12 tt]=47 time=211 ms
^C
www.boardcon.com ping statistics
13 packets transmitted. 11 received. 15.3846% packet loss, time 16277ms
rtt min/avg/max/mdev = 211.033/212.315/214.843/1.009 ms
root@linaro-alip:/#
root@linaro-alin:/# ping -T eth1 www boardcon com
PTNG www.boardcon.com (67 222 54 196) from 192 168 0 138 eth1: 56(84) bytes of data
64 bytes from $67-222-54-186$ unified aver com $(67, 222, 54, 186)$; jcm segel tt]=47 time=214 ms
64 bytes from $67_{222} = 54_{-196}$ unified aver com $(67_{-222} = 54_{-196})$; imp_seq=2 t1=47 time=14 ms
64 bytes from $67-222-54-196$ unified aver com $(67,222,54,196)$. Tomp seq=3 ttl=47 time=213 ms
64 bytes from $67-222-54-196$ unified aven com $(67,222,54,196)$. Timp_seq=5 tt1=47 time=213 ms
64 bytes from 67-222-54-196 unified layer com (67-222-54.196). Complete studies from 67-222-54-196 unified layer com (67-222-54.196). Complete studies from 67-212 ms
64 bytes from $67-222-54-196$ until red aver com $(67.222.54.196)$; 1cmp_seq=6 tt = 47 time=212 ms
64 bytes from 67-222-54-196 unified ayer.com (67.222.54.196): 1cmp_seq=7 ttl=47 time=212 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): 1cmp_seq=8 ttl=47 time=212 ms
64 bytes from 67-222-54-196.unified ayer.com (67.222.54.196): icmp_seq=9 tt1=47 time=212 ms
64 bytes from 6/-222-54-196.unifiedlayer.com (6/.222.54.196): jcmp_seq=10 ttl=4/ time=212 ms
64 bytes from 67-222-54-196.unified[ayer.com (67.222.54.196): icmp_seq=11 tt]=47 time=212 ms
64 bytes from 67-222-54-196.unified[ayer.com (67.222.54.196): icmp_seq=12 tt]=47 time=212 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=13 ttl=47 time=212 ms
^C
www.boardcon.com ping statistics
13 packets transmitted, 12 received, 7.69231% packet loss, time 12027ms
rtt min/avg/max/mdev = 211 639/212 371/214 058/0 561 ms
1 cc m m / avg/max/macv = c11.005/c12.071/c14.000/0.001 mb

6.6 RTC

Execute the follow command to set the RTC time:

date -s "2023-06-20 10:22:00"

hwclock -w

hwclock

root@linaro-alip:/# date -s "2023-06-20	10:22:00"
date: invalid date '2023-06-20 10:22:00	
root@linaro-alip:/# hwclock -w	
root@linaro-alip:/# hwclock	
2023-06-20 02:22:08.874674+00:00	
root@linaro-alip:/# hwclock	
2023-06-20 02:22:32.843655+00:00	
root@linaro-alip:/# hwclock	
2023-06-20 02:23:00.434740+00:00	
root@linaro-alip:/#	

If it takes time to be saved after power failure, please connect the button battery.

6.7 4G(EC20)

Step 1, execute the following command to realize ppp dialing:

pppd call quectel-ppp &



got it
send (ATD*99#^M) expect (CONNECT) ^M
CONNECT got it
Script chat -s -v -f /etc/ppp/peers/quectel-chat-connect finished (pid 2069), status = 0x0 Serial connection established. using channel 1 Using interface ppp0
Connect: ppp0 ←>/dev/ttyUSB3
<pre>sent [LCP ConfAck id=0x1 <asyncmap 0x0=""> <magtc 0x9083031=""> <pre>cpcomp> (accomp>] sent [LCP ConfAck id=0x0 <asyncmap 0x0=""> <auth chap="" md5=""> <magtc 0xd0ffd81b=""> <pcomp> <accomp>] rcvd [LCP ConfAck id=0x1 <asyncmap 0x0=""> <auth chap="" md5=""> <magtc 0xd0ffd81b=""> <pcomp> <accomp>] sent [LCP ConfAck id=0x1 <asyncmap 0x0=""> <magtc 0x90083d31=""> <pcomp> <accomp>] sent [LCP ConfAck id=0x0 magtc=0x90083d31]</accomp></pcomp></magtc></asyncmap></accomp></pcomp></magtc></auth></asyncmap></accomp></pcomp></magtc></auth></asyncmap></pre></magtc></asyncmap></pre>
<pre>rcvd [LCP DiscReq id=0x1 magic=0xd0frd8ib] rcvd [CHAP Challenge id=0x1 <43d9b9cbde2f79d1322750e45f3af0eb>, name = "UMTS_CHAP_SRVR"] sent [CHAP Response id=0x1 <e901d6812caedab20322f36a7ecdf888>, name = "test"] rcvd [LCP EchoRep id=0x0 magic=0xd0ffd81b 90 08 3d 31] rcvd [CHAP Success id=0x1 ""]</e901d6812caedab20322f36a7ecdf888></pre>
CHAP authentication succeeded
sent [IPV6CP ConfReq id=0x1 <addr 0.0.0.0=""> <ms-dns1 0.0.0.0=""> <ms-dns2 0.0.0.0="">] sent [IPV6CP ConfReq id=0x1 <addr fe80::0480:8be6:0cde:5c36="">]</addr></ms-dns2></ms-dns1></addr>
sent [IPCP ConfNak id=0x0 <addr 0.0.0.0="">]</addr>
<pre>rcvd [IPCP ConfNak id=0x1 <addr 10.65.22.200=""> <ms-dns1 120.80.80.80=""> <ms-dns2 221.5.88.88="">] sent [IPCP ConfReq id=0x2 <addr 10.65.22.200=""> <ms-dns1 120.80.80.80=""> <ms-dns2 221.5.88.88="">] rcvd [IPCP ConfReq id=0x1] cont [IPCP ConfReq id=0x1]</ms-dns2></ms-dns1></addr></ms-dns2></ms-dns1></addr></pre>
rcvd [IPCP ConfAck id=0x2 <addr 10.65.22.200=""> <ms-dns1 120.80.80.80=""> <ms-dns2 221.5.88.88="">] Could not determine remote IP address: defaulting to 10.64.64.64</ms-dns2></ms-dns1></addr>
Script /etc/ppp/ip-pre-up finished (pid 2078), status = 0x0
not replacing default route to eth0 [192.168.0.2] local IP address 10.65.22.200
remote IP address 10.64.64.64
primary DNS address 120.80,80.80
Secondary DNS address 221.5.80.80
Script /etc/ppp/ip-up finished (pid 2081), status = 0x0
sent [IPV6CP ConfReq id=0x1 <addr fe80::0480:8be6:0cde:5c36="">]</addr>
sent [IPV6CP ContReq id=0x1 <addr fe80::0480::8be6:0cde::5c36="">]</addr>
sent [IPV6CP ConfReq id=0x1 <addr fe80::0480:80e6:0cde:5c36="">] sent [IPV6CP ConfReq id=0x1 <addr fe80::0480:8be6:0cde:5c36="">]</addr></addr>

Step 2, execute the following to view the network interface status:

ifconfig -a

```
ppp0: flags=4305<UP,POINTOPOINT,RUNNING,NOARP,MULTICAST> mtu 1500
inet 10.65.22.200 netmask 255.255.255 destination 10.64.64.64
ppp txqueuelen 3 (Point-to-Point Protocol)
RX packets 4 bytes 52 (52.0 B)
RX errors 0 dropped 0 overruns 0 frame 0
TX packets 10 bytes 142 (142.0 B)
TX errors 0 dropped 0 overruns 0 carrier 0 collisions 0
```

Step 3, execute the following to check the connectivity of the detection network:

ping -I ppp0 www.boardcon.com

root@linaro-alip:~#
root@linaro-alip:~# ping -I ppp0 www.boardcon.com
PING www.boardcon.com (67.222.54.196) from 10.65.22.200 ppp0: 56(84) bytes of data.
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=1 ttl=47 time=289 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=2 ttl=47 time=202 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=3 ttl=47 time=207 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=4 ttl=47 time=198 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=5 ttl=47 time=197 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=6 ttl=47 time=210 ms
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=7 ttl=47 time=197 ms

6.8 GPS(EC20)

Plug the EC20 module and connect GPS antenna, then power on and execute the follow command:

echo -e "AT+QGPS=1\r\n" > /dev/ttyUSB2

cat /dev/ttyUSB1



<pre>root@linaro-alip:~# echo -e "AT+QGPS=1\r\n" > /dev/ttyUSB2 root@linaro-alip:~# cat /dev/ttyUSB1 \$GPVTG,,T,,M,,N,,K,N*2C</pre>
\$GPGSA,A,1,,,,,,,,,,,,*1E
\$GPGGA,,,,,,0,,,,,,*66
\$GPRMC,,V,,,,,,,,N*53
\$GPVTG,,T,,M,,N,,K,N*2C
\$GPGSA,A,1,,,,,,,,,,,,*1E
\$GPGGA,,,,,,0,,,,,,*66
\$GPRMC,,V,,,,,,,,,N*53
\$GPVTG,,T,,M,,N,,K,N*2C
\$GPGSA,A,1,,,,,,,,,,,,*1E
\$GPGGA,,,,,,0,,,,,,*66
\$GPRMC,,V,,,,,,,,N*53
\$GPVTG,,T,,M,,N,,K,N*2C
\$GPGSA,A,1,,,,,,,,,,,,*1E
\$GPGGA,,,,,,0,,,,,,*66
\$GPRMC,,V,,,,,,,,,N*53
\$GPGSV,1,1,03,33,,,34,38,,,34,39,,,34*7C
\$GPVTG,,T,,M,,N,,K,N*2C
\$GPGSA,A,1,,,,,,,,,,,,*1E
\$GPGGA,,,,,,0,,,,,,*66
\$GPRMC,,V,,,,,,,,,N*53
\$GPGSV,1,1,04,33,,,34,38,,,34,39,,,34,40,,,34*78
\$GPVTG,,T,,M,,N,,K,N*2C
\$GPGSA,A,1,,,,,,,,,,,,*1E
\$GPGGA,,,,,,0,,,,,,*66
\$GPRMC,,V,,,,,,,N*53

6.9 WiFi

Step 1, connect the WiFi antenna, then click the network icon in the top right corner of the UI interface, select the SSID from the list of available networks and enter the password.



	- S - S -	()) Thu 22 Dec, 12:18	linaro
Ethernet Network			
disconnected			
Wi-Fi Networks (p2p0)			
disconnected			
Available			
AP50003153	b d		
Boardcon	10 6		
Boardcon_WIFI	lluf		
DIRECT-4A-HP Laser 136w	ini ⁰		
DIRECT-c8-HP M130 LaserJet	1		
More networks	•		
Wi-Fi Networks (wlan0)			
disconnected			
Available			
AP50003153	1 11		
Boardcon	lluf.		
Boardcon_WIFI	1		
DIRECT-4A-HP Laser 136w	10		
DIRECT-c8-HP M130 LaserJet	1		
More networks	•		
Connect to Hidden Wi-Fi Networ	k		
Create New Wi-Fi Network			
VPN Connections	•		

Step 2, execute the following to view the network interface status:

ifconfig

Ĩ	116.831201] IPv6: ADDŔCONĠ(NETDEV_CHANGE): wlx367de41c8192: link becomes ready
wl>	x367de41c8192: flags=4163 <up,broadcast,running,multicast> mtu 1500 inet 192.168.0.146 netmask 255.255.255.0 broadcast 192.168.0.255 inet6 fe80::da4b:cda8:d7b:b1a9 prefixlen 64 scopeid 0x20<link/> ether 36:7d:e4:1c:81:92 txqueuelen 1000 (Ethernet) RX packets 45 bytes 6079 (5.9 KiB) RX errors 0 dropped 4 overruns 0 frame 0 TX packets 18 bytes 2811 (2.7 KiB) TX errors 0 dropped 0 overruns 0 carrier 0 collisions 0</up,broadcast,running,multicast>

Step 3, execute the following to check the connectivity of the detection network:

ping -I wlx367de41c8192 www.boardcon.com

root@linaro-alip:/# ping -I wlx367de41c8192 www.boardcon.com						
PING www.boardcon.com (67.222.54.196) from 192.168.0.146 wlx367de41c8192: 56(84) bytes of data.						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=1 ttl=47 time=205 ms						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=2 ttl=47 time=204 ms						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=3 ttl=47 time=209 ms						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=4 ttl=47 time=204 ms						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=5 ttl=47 time=203 ms						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=6 ttl=47 time=203 ms						
64 bytes from 67-222-54-196.unifiedlayer.com (67.222.54.196): icmp_seq=7 ttl=47 time=207 ms						

6.10 Bluetooth

Step 1, click the Buletooth icon in the top right corner of the UI interface, Select "**Devices**" enter the Buletooth Devices.



Customize the embedded system based on Your Idea



Step 3, click the "Search" button to start searching and select the available device in the list to pair.



Step 4, the Bluetooth device name is hidden by default. Set the Bluetooth device name to be visible according to the following settings:





6.11 CAN

CAN Step 2, execute the following commands on the serial terminal of board A and board B respectively. # ip link set can0 down # ip link set can0 type can bitrate 1000000 dbitrate 3000000 fd on # ip link set can0 up Step 3, execute the following instructions as the receiver. # candump can0 // set CAN0 as receive Step 4, execute the following commands as the transmitter. # cansend can0 123#DEADBEEF // CAN0 send characters 0xDE 0xAD 0xBE 0xEF 🛩 serial-com11 🛛 🗙 △ ▷ 🖋 serial-com4 🗙 root@linaro-alip:~# ip link set can0 down root@linaro-alip:~# ip link set can0 type can bitrate 1000000 dbitrate 3000000 ot@linaro-alip:~# ip link set can0 down ot@linaro-alip:~# ip link set can0 type can bitrate 1000000 dbitrate 3000 root@linaro-alip:-# ip link set can0 type can bitrate 1000000 dbitrate fd on [1081.331404] rockchip_canfd fea50000.can can0: bitrate error 1.0% RTNETINK answers: Operation not supported root@linaro-alip:# ip link.set can0 up root@linaro-alip:# candump can0 can0 123 [4] DE AD BE EF root@linaro-alip:# cansend can0 123#DEADBEEF root@linaro-alip:# cansend can0 123#DEADBEEF root@linaro-alip:# cansend can0 123#DEADBEEF root@linaro-alip:# cansend can0 123#DEADBEEF ootelinaro-alip:-# ip link set can0 type can bitrate 1000000 optrate 00 fd on 122.396368] rockchip_canfd fea50000.can can0: bitrate error 1.0% TMELINK answers: Operation not supported ootelinaro-alip:-# ip link set can0 up 130.563667] IPV6: ADDRCOMF(NETDEV_CHANGE): can0: link becomes ready ootelinaro-alip:-# cansend can0 123#DEADBEEF can0 123 [4] DE AD BE EF can0 123 [4] DE AD BE EF can0 123 [4] DE AD BE EF

6.12 RS485

Step 1, connect the RS485 ports of board A and board B as follows:							
Board A	A Contraction of the second se	● B ● A ● GND	Board B				

Step 1, connect the CAN ports of board A and board B as follows:



Step 2, execute the following commands on the terminal serial ports of board A and board B respectively.

com /dev/ttyS0 115200 8 0 1

Step 3, at this time, you can test the RS485 communication.

Serial=COM4-485	Seriel-COM5
MMCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	root@linano_alip:/#
	port = /dev/ttys0
	baudrate = 115200
	CS = 8 parity = 0
	stopb = 1
	[5208.510677] of_dma_request_slave_channel: dma-names property of node '/serial@fdd50000' missing or emp
	[^y 5208.510785] dw-apb-uart fdd50000.serial: failed to request DMA, use interrupt mode
	RECV: H

6.13 UART

Step 1, short circuit RX and TX pins of UART.



Step 2, UART4 test:

com /dev/ttyS4 115200 8 0 1



Step 3, UART5 test:

com /dev/ttyS5 115200 8 0 1





6.14 SPI

Step 1, short circuit MISO_M1 and MOSI_M1 pins of SPI.



Step 2, SPI0 test:

Step 3, SPI2 test:



6.15 Camera

Step 1, camera preview command:

TV1

gst-launch-1.0 v4l2src device=/dev/video8 ! video/x-raw,format=NV12,width=1280,height=720, framerate=30/1 ! xvimagesink &

TV2

gst-launch-1.0 v4l2src device=/dev/video9 ! video/x-raw,format=NV12,width=1280,height=720, framerate=30/1 ! xvimagesink &

TV3

gst-launch-1.0 v4l2src device=/dev/video10 ! video/x-raw,format=NV12,width=1280,height=720, framerate=30/1 ! xvimagesink &

TV4

gst-launch-1.0 v4l2src device=/dev/video11 ! video/x-raw,format=NV12,width=1280,height=720, framerate=30/1 ! xvimagesink &

Step2, camera video recording command:

gst-launch-1.0 v4l2src device=/dev/video8 num-buffers=100 ! \

video/x-raw,format=NV12,width=1280,height=720,framerate=30/1 ! \

videoconvert ! mpph264enc ! h264parse ! mp4mux ! \

filesink location=/tmp/h264a.mp4

Step3, camera take a picture command:

gst-launch-1.0 -v v4l2src device=/dev/video8 num-buffers=10 ! \

video/x-raw,format=NV12,width=1280,height=720 ! mppjpegenc ! \

multifilesink location=//tmp//test1%05d.jpg